

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re Application of :**

Guy Dupenloup

**Serial No. :**

09/026,790

**Filed :**

February 20, 1998

**For :**

Automatic Synthesis Script  
Generation For Synopsys Design  
Compiler

**Group Art Unit :**

2825

**Examiner :**

Thompson, Annette M.

**Atty Docket :**

30454-122 / P-3605

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Mark Salvatore

November 12, 2004

Date

Signature

**SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85**

**Official Draftsman**

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

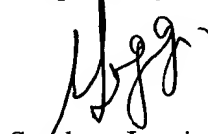
Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milipitas, CA 95035  
408-433-7475

Date: 11/11/04

Respectfully submitted,



Sandeep Jaggi

Reg. No. 43,331